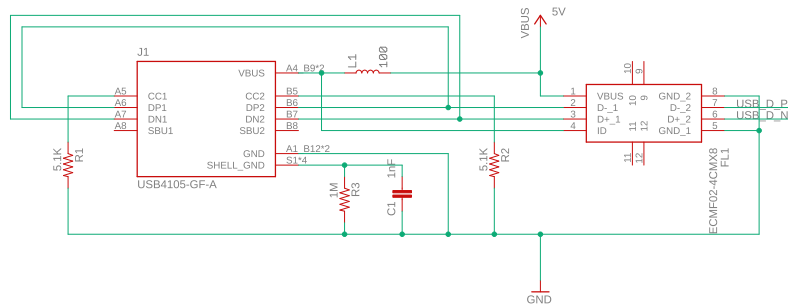


USB Connector



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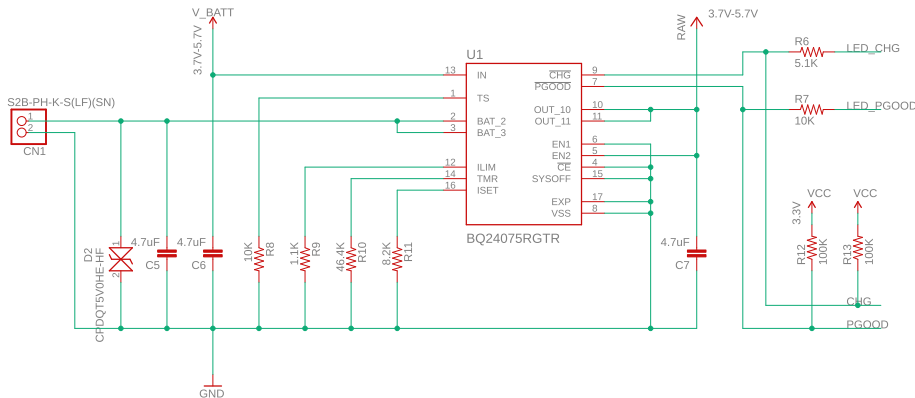
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Battery Charging



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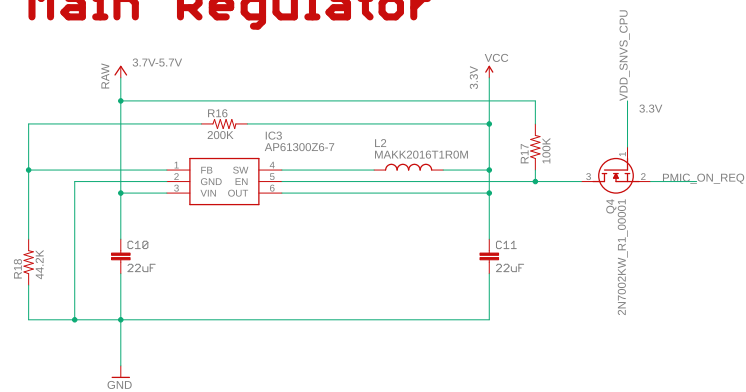
SNVS Reg

The diagram illustrates the SNVS Regulator circuit. The main regulator IC2 (TCR2LF33,LM_CT) is powered by VIN (3.7V-5.7V) and GND, with a 4.7uF capacitor (C8) on the input. Its VOUT (NC) is connected to VDD_SNVS (3.3V). The IC2 is also connected to a control pin (5) and a feedback pin (4). The VDD_SNVS (3.3V) is connected to the A1/A2 pin of the D15 (BAT54CLT3G) diode, which is also connected to VDD_SNVS_CPU (3.3V). The BAT_3.3V and BAT_GND pins are connected to the TP1 and TP2 test points. The KMS231GLFS diode (S2) is connected to the NO_1/COM_3 and COM_1/NO_2 pins, with a 10K resistor (R70) connected to the COM_2 pin and the ONOFF pin. The 2N7002KWL_R1_00001 MOSFET (Q2) is connected to the WAKEUP pin (2) and the P11_WAKEUP pin (3). The 2N7002KWL_R1_00001 MOSFET (Q3) is connected to the ONOFF pin (2) and the PMIC_ONOFF pin (3). The RAW pin is connected to the 3.7V-5.7V input, and the RAW pin is connected to the 3.7V-5.7V input.

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Main Regulator



※ PMIC_REG_ON is level shifted to ensure regulator goes into PFM mode

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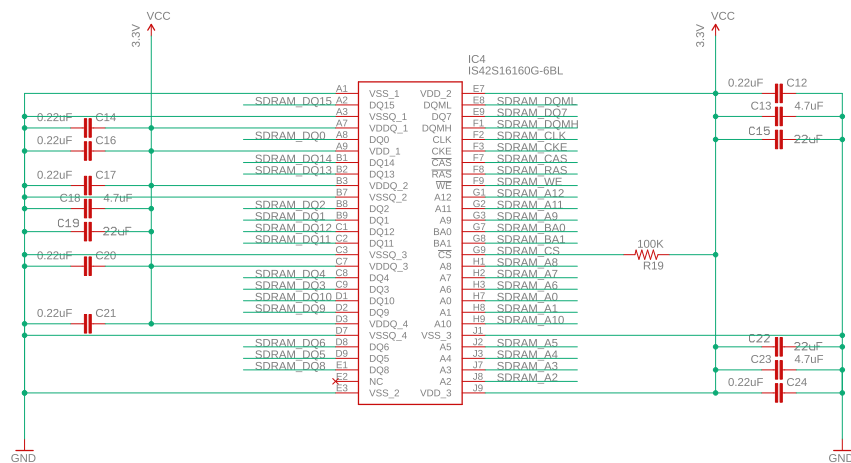
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16-BIT SDRAM

* CKE is pulled down by 100K on the RT1062



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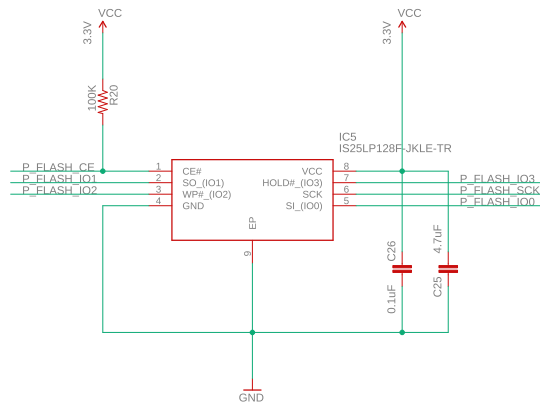
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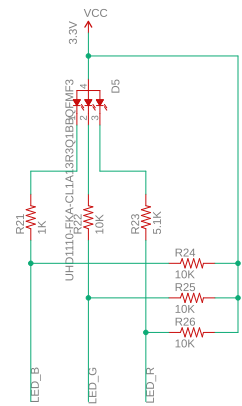
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Program QSPI Flash



RGB LED



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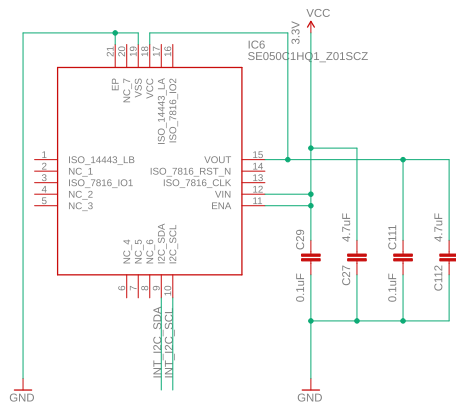
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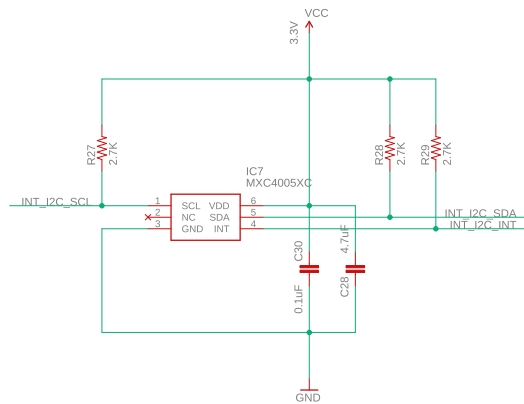
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Security



Accelerometer



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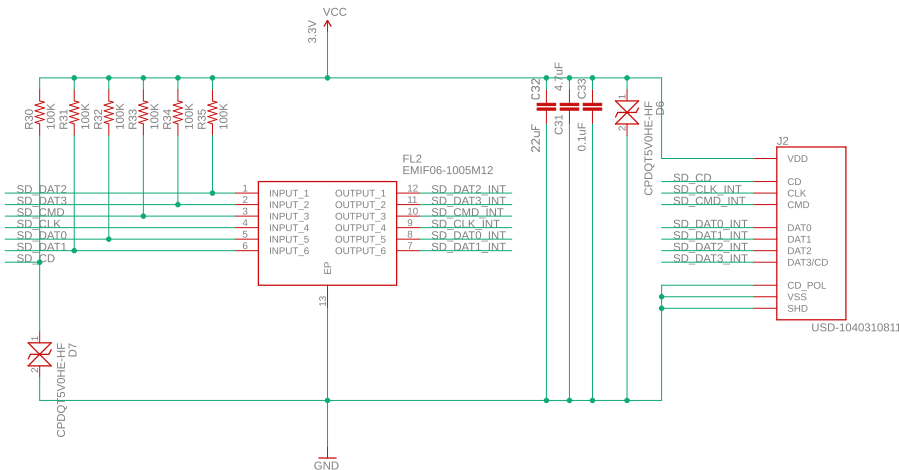
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SD CARD



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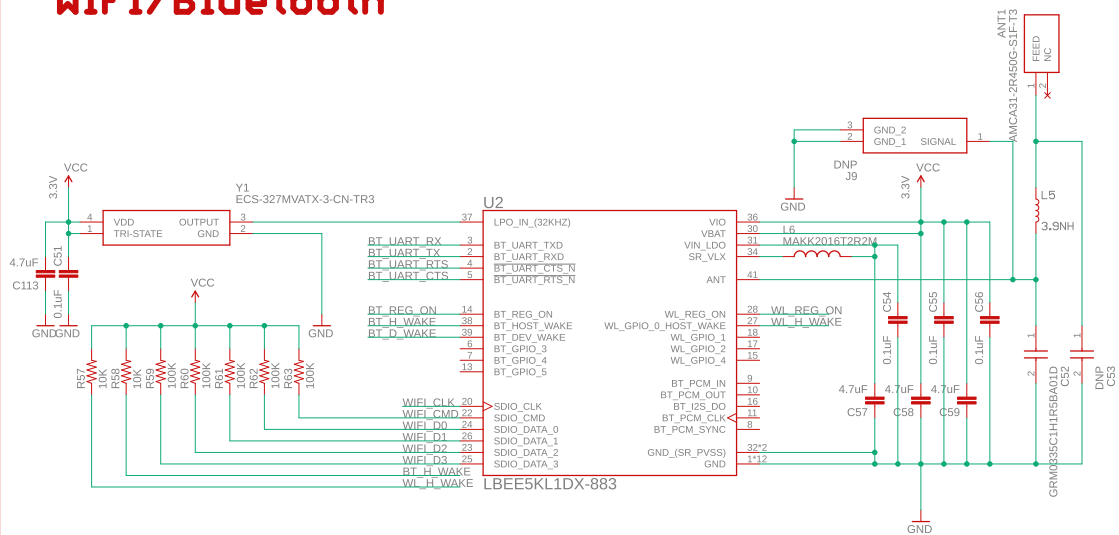
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WiFi/Bluetooth



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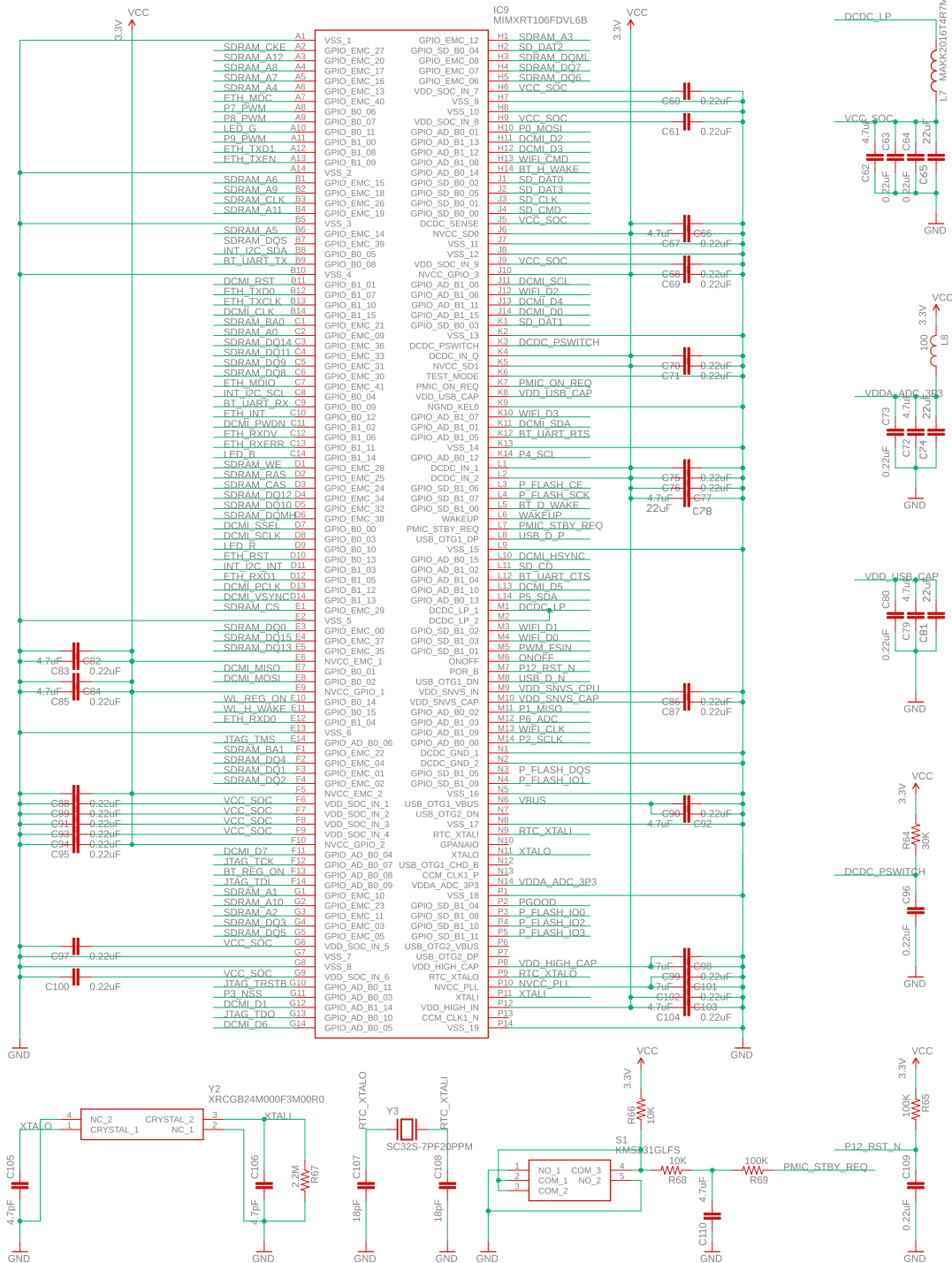
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MIMXRT1062



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